



**MODIFICATION OF STANDARD
KEPCO MODEL BOP 100-1MC
KEPCO MODEL BOP 100-1DC**

The Kepco Models BOP 100-1MC and BOP 100-1DC have been modified from standard Kepco Models BOP 100-1M and BOP 100-1D, respectively, to be stable handling capacitive loads up to 10 mF.

INSTRUCTION MANUAL CORRECTIONS:

This modification makes the BOP more suitable for a wide variety of applications such as solar cell/ solar panel testing, piezoelectric device driving/ testing, capacitor testing, capacitive transducer driving/testing and powering industrial or lab-type applications for capacitive or capacitive-resistive loads.

Static specifications representing the unit's accuracy in Voltage mode are identical with the standard BOP models, while the ripple and noise specifications are better (approximately 50% lower) for MC and DC models than standard BOP models.

Specifications listed in Table 1 are for BOP 100-1MC and BOP 100-1DC in Voltage Mode.

TABLE 1. BOP 100-1MC AND BOP 100-1DC SPECIFICATIONS

Bandwidth (DC to f-3dB)		Rise/Fall Time ⁽²⁾	Recovery at Step Load ⁽³⁾
Resistive Load, Nominal	Capacitive Load, 10 μ F ⁽¹⁾		
2.2 kHz	3.5 kHz	165 μ S	185 μ S

NOTES:

- (1) Nonuniformities of the frequency response for the standard 10 μ F load create a larger 3-dB bandwidth than for the resistive load.
- (2) 10% to 90%, with nominal resistive load.
- (3) Load between infinity and nominal resistive load values.

In voltage mode the bandwidth of the BOP 100-1MC and DC models is less than the standard BOP, while the response time is increased. Nonuniformities of the frequency response can be nearly eliminated by reducing the bandwidth in Voltage mode.

Reducing Bandwidth. Bandwidth can be reduced in a predictable manner by increasing the internal compensation capacitance by placing an external capacitor (see Table 2) in parallel with the main compensation capacitor of Assembly A1.

- For older models, identified by the indicator between the two front panel meters labeled REMOTE, the external capacitor is soldered across A1C21 (see Figure 1).
- For newer models, identified by the indicator between the two front panel meters labeled REMOTE (W DIG ON)/OUT ON (W DIG OFF), connect the external capacitor across pins 12 and 14 of the rear programming connector.

TABLE 2. BANDWIDTH CORRECTION

	EXTERNAL CAPACITOR (ACROSS A1C21, SEE FIGURE 1)						
	1 nF	2.2 nF	4.7 nF	15 nF	33 nF	47 nF	100 nF
CORRECTED BANDWIDTH	2.18 kHz	2.15 kHz	2.05 kHz	1.55 kHz	0.97 kHz	0.77 kHz	0.42 kHz

NOTE: Listed bandwidth correction values are for Voltage mode, nominal resistive load.

In Current Mode the dynamic specifications are: 3-dB bandwidth of 3kHz and rise/ fall time of 115 μ s.

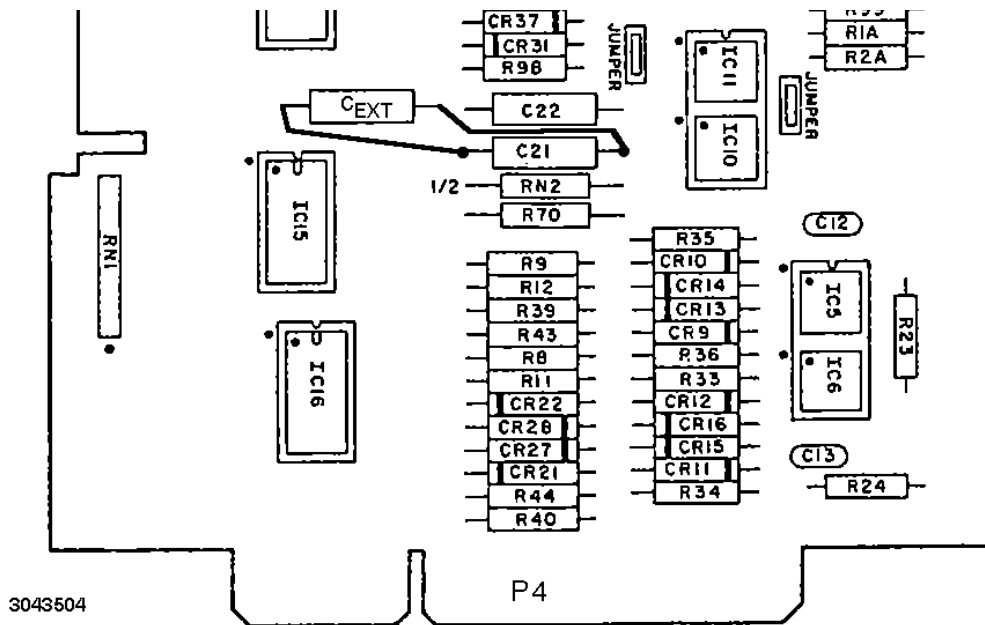


FIGURE 1. ASSEMBLY A1, EXTERNAL BANDWIDTH CORRECTION CAPACITOR LOCATION (OLDER MODELS ONLY)